(51) International Patent Classification[7]: H03F 1/32, H04B 1/04

(21) International Application Number: PCT/GB01/00360

(22) International Filing Date: 29 January 2001 (29.01.2001)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
0003666.5   17 February 2000 (17.02.2000)   GB

(71) Applicant (for all designated States except US): WIRE-LESS SYSTEMS INTERNATIONAL LIMITED [GB/GB]; Innovation House, Bristol Business Park, Coldharbour Lane, Bristol BS16 1EJ (GB).

(72) Inventors; and
(75) Inventors/Applicants (for US only): RING, Steven, R. [GB/GB]; 35 Sturmer Close, Yate, Bristol BS37 5UK (GB). DANN, Ronald, G. [GB/GB]; 125 Belmont Road, St Andrews, Bristol BS6 5AR (GB).

(74) Agents: HOGG, Jeffery, Keith et al.; Withers & Rogers, Goldings House, 2 Hays Lane, London SE1 2HW (GB).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:
— with international search report
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

(54) Title: DISTORTION DETECTION FOR A POWER AMPLIFIER
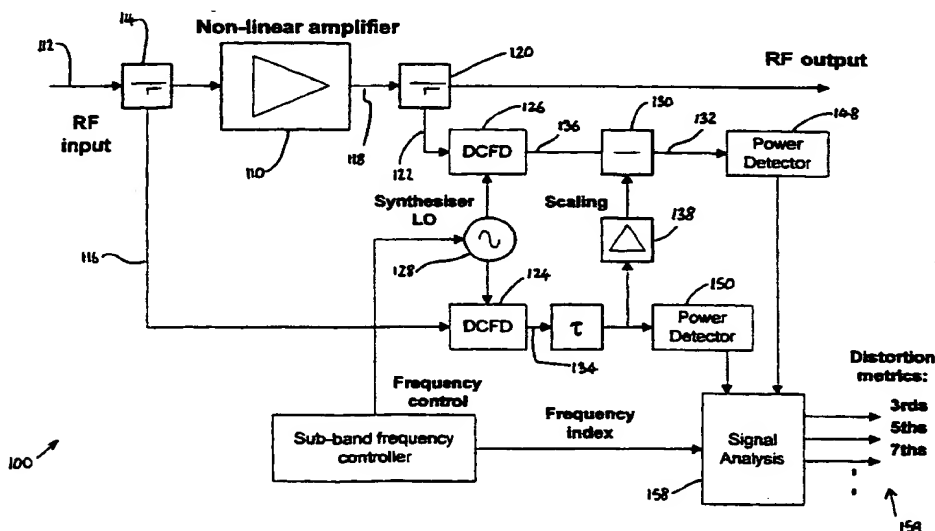
(57) Abstract: The input and output of amplifier (110) are sampled and downconverted in frequency and filtered at (124 and 126) to produce sub-band signals. The input signal sub-band (134) is then subtracted from the output signal sub-band (136) at (130) to produce a residual signal (132) containing any distortion present in the sub-band selected by the downconversion oscillator (128). Signal analyser (158) compares the input and output signal energy to deduce the amount of distortion in the output signal, to provide signals for controlling a distortion reduction mechanism such as a predistorter operating on amplifier (110). Alternatively, the input signal sub-band (134) and the output signal sub-band (136) are Fourier transformed (210 and 212) to produce spectrums which can be analysed to determine the presence of distortion in the output of amplifier (110).

WO 01/61841 A1

DISTORTION DETECTION FOR A POWER AMPLIFIER

This invention relates to methods and apparatus for detecting distortion in an output signal which a signal handling means (such as an amplifier) produces in response to an input signal.

It is known to use distortion reduction mechanisms on amplifiers in an attempt to linearise the amplifier's response. It is often desirable to monitor residual distortion in the output of the amplifier, and to use this signal to adapt the distortion reduction mechanism to maximise the cancellation of the distortion. It is known to use a pilot signal to detect residual distortion. The pilot signal is added to the input signal and detected in the output signal so that any distortion it experiences can be assessed. The use of pilot signals for distortion detection has the disadvantage that a pilot signal must be generated, detected and then removed from the output signal afterwards. Thus, the use of pilot signals increases system complexity and adds the risk that spurious noise or continuous wave (CW) emissions introduced by the pilot signal detection system will remain as unwanted residues in the amplifier output.

An object of the invention is to provide the detection of distortion without the use of pilot signals and ameliorate at least some of the problems identified above.

According to one aspect, the invention provides a method of detecting distortion in an output signal which a signal handling means produces in response to an input signal, the method comprising isolating as a first signal a frequency range of the input signal and as a second signal, a frequency range of the output signal, and comparing the first and second signals to assess the distortion in the output signal.

According to another aspect, the invention provides apparatus for detecting distortion in an output signal which a signal handling means produces in response to an input signal, the apparatus comprising means for isolating as a first signal a frequency range of the input

signal and as a second signal a frequency range of the output signal, and comparing means for comparing the first and second signals to assess the distortion in the output signal.

By comparing the input and output signals in this way, the presence of distortion in the output signal can be assessed, and a pilot signal is not used.

In the preferred embodiment, the frequency ranges are substantially the same, and the isolating step may involve frequency converting the input and output signal to produce the first and second signals. Preferably, the frequency conversion is frequency down-conversion which means that the resulting signals are of lower frequency and therefore provide less demands on the devices used to handle them. Advantageously, the same signal can be used to frequency down-convert both the input and output signals.

The isolating step may be performed at a series of different frequency ranges. The series may be such that at least two adjacent ranges overlap or abut one another. Alternatively, it can be arranged that at least two of the ranges are discontinuous, so that complete frequency coverage is not provided. In one embodiment, the isolating step traverses the series of ranges in a sequence which is not in frequency order. This may help to avoid interactions between the temporal characteristics of the input signal and the isolating step sequence.

Frequency ranges used in the isolation step may be fixed or chosen to cover a frequency range of interest (for example, to target an active part of the frequency band of the input signal). In a preferred embodiment, at least one frequency range used in the isolation step can be chosen dynamically.

Advantageously, the comparing step comprises subtracting the first signal from the second signal to produce a residual signal. The residual signal may contain substantially only residual distortion that has been caused by the signal handling means. The signals involved in the subtraction may need to be scaled relative to one another so that the wanted signals (i.e. those which are ideally to be obtained from the signal handling means) cancel

leaving only the distortion which it is desired to detect.   Such scaling of the signals may be needed where the signal handling means amplifies the input signal.

The comparison step may involve the correlation of the residual signal with a reference distortion signal to produce a signal indicative of distortion in the residual signal corresponding to the reference distortion signal.   Preferably, the reference distortion signal is derived from the first signal (which is part of the input signal).   The reference distortion signal may be derived by multiplying the first signal with itself repeatedly.

The comparison step may involve transforming the residual signal to the frequency domain for further analysis.   In this way, the spectrum of the residual distortion can be analysed. Preferably, the first signal is also transformed to the frequency domain (e.g. by FFT), thus allowing comparisons to be made between the two spectra.   For example, the spectrum of the first signal may be used to identify any remaining wanted signal components which have not been removed from the residual distortion signal or to provide a reference against which the magnitude of components in the residual distortion spectrum can be measured.

In a particularly preferred embodiment, the inventive method and apparatus for detecting distortion is used to control a distortion reduction mechanism operating on a distorting signal handling means.   In this way, the distortion reduction mechanism may be adapted to maximise the cancellation of the distortion created by the signal handling means.   For example, the distortion reduction mechanism could be a predistortion lineariser or a feed-forward lineariser.

By way of example only, certain embodiments of the invention will now be described with reference to the accompanying figures, in which:

Figures 1 and 2 each schematically illustrate distortion detecting apparatus applied to a non-linear amplifier.

Distortion detection scheme 100 in Figure 1 operates on non-linear amplifier 110. Amplifier 110 is a wide band radio frequency power amplifier having a non-linear

characteristic such that it imposes intermodulation distortion upon signals passing through it. The distortion detection scheme 100 provides signals indicative of this distortion which can be used by an appropriate distortion reduction mechanism (not shown), for example, a predistorter operating on the input 112 to amplifier 110, subsequent to coupler 114.

The input signal 112 is sampled at coupler 114 along path 116. The output 118 of the amplifier 110 is sampled at coupler 120 along path 122. The signals on paths 116 and 122 are frequency down-converted and filtered by Down Conversion, Filtering and Digitisation (DCFD) units 124 and 126 respectively. The downconversion performed by units 124 and 126 is performed by mixing the incoming signal with a local oscillator (LO) signal from frequency controlled synthesiser 128. The digital output of DCFD 126 therefore corresponds to a coincident sample of a sub-band of output signal 118. Similarly, the digital output of DCFD 124 corresponds to a sample of the same sub-band of input signal 112.

It will be appreciated that DCFD units 124 and 126 can be adjusted to pass a different sub-band from the input signal 112 and the amplifier output signal 118 respectively. This can be achieved by adjusting the frequency of the LO signal from synthesiser 128 so that different frequency ranges are mixed down to the filter passbands within DCFD units 124 and 126. Hence, the frequency band of the input and output signals can be divided into a number of sub-bands which can be evaluated in turn by adjusting the frequency of the output of synthesiser 128. The desired sub-bands could be overlapping or abutting so as to cover the whole frequency band of interest but, on the other hand, it might be that they do not cover the whole of the frequency band of the input and output signals in a contiguous fashion. It is possible for the desired sub-bands to be assigned dynamically by, for example, detecting only those sub-bands corresponding to portions of the frequency band of the input and output signals which contain signal energy at a particular time.

Since it is the distortion present in signal 118 which is of interest, the detection scheme 100 forms a signal indicative of this distortion. This is achieved by using subtractor 130 to produce a residual signal 132. Subtractor 130 subtracts the input signal sub-band 134

provided by DCFD 124 from the output signal sub-band 136 provided by DCFD unit 126. This subtraction process cancels components in sub-band 136 which correspond to actual components of the input signal 112 to amplifier 110. The residual signal 132 therefore contains substantially only components of sub-band 136 which correspond to distortion present in the output 118 of amplifier 110. To ensure the cancellation of components of input signal 112 in sub-band 136, sub-band 134 is scaled by scaler 138 which matches the amplitude of components due to input signal 112 in the two sub-band signals 134 and 136 (the signal 118 being an amplified version of signal 112).

The power of the residual signal 132 is then measured using a power detector 148 to determine the amount of distortion in the output 118 of amplifier 110 within a given sub-band. A complete picture of distortion contained in the amplifier output 118 can then be acquired by stepping through the sub-bands by changing the frequency of the LO signal produced by synthesiser 128. Power detector 150 measures the power present within a given sub-band of the input signal 112.

The pattern of distortion energies measured in the various sub-bands across the frequency band of interest and the corresponding pattern of input signal energies are then acquired by the signal analyser 158. The signal analyser 158 can determine the amount of third-order and other orders of distortion present in the amplifier output signal 118 based upon the acquired data. The signal analyser's measurements 159 of third-order and higher order distortion components can then be used to control an amplifier linearisation mechanism, for example, a predistorter or a feed-forward linearisation system.

It will be appreciated that many of the signal processing functions illustrated in the block diagrams of Figure 1 and Figure 2, including parts of the DCFD units and the synthesiser, can be implemented digitally as well as by using analogue techniques. It will also be appreciated that the invention apparatus is particularly suited to integration, since breaking the frequency band of interest into narrower sub-bands allows the use of integrated analogue to digital converters and digital signal processing techniques.

The distortion detection scheme 200 shown in Figure 2 is more sophisticated than scheme 100 and can advantageously use similar hardware as was used in scheme 100, (and therefore keep costs substantially the same) but it can exploit more powerful signal processing techniques to analyse the distortion products contained in the amplifier output 118. The distortion detection scheme 200 of Figure 2 is similar in many ways to detection scheme 100. Therefore, elements of detection scheme 200 already described with reference to detection scheme 100 retain the same reference numerals and will not be described again in detail in the following discussion.

In detection scheme 200, the output signal sub-band 136 and the input signal sub-band 134 are each translated from the time domain to the frequency domain by, for example, a digital Fourier transformation process, indicated by FFT units 210 and 212. The spectra 214 and 216 for a given sub-band are acquired by a signal analyser 220. The signal analyser 220 records the nature of the input spectrum based upon the spectrum 216. A vector subtraction step within the signal analyser 220 performs prescaling and vector difference operations similar to the scaling operations involving scaler 138 and subtractor 130 in Figure 1. The input power and distortion vector error data in each sub-band can be used by the signal analysers 220 to distinguish and quantify distortion errors arising from different orders of intermodulation distortion and phase distortion in the amplifier 110.

Based upon the pattern of distortion error vectors in each sub-band, the signal analyser 220 can determine which order of amplifier non-linearity, for example, third order, fifth order, etc., is responsible for a given measured distortion product. Consider, for example, an amplifier amplifying multiple modulated carriers. To distinguish and quantify the amount of third order distortion present in the amplifier output signal 118, the signal analyser 220 first examines the spectrum of the input signal 216 in each sub-band to determine the frequencies and powers of the incoming carrier or carriers. Using this information, the signal analyser 220 computes a set of sub-bands and frequencies where it expects to find significant third order distortion products. The signal analyser 220 then examines the sub-bands and frequencies in the distortion vector error data where it expects to find third order distortion products. The signal analyser 220 then collects the amounts of third order distortion energy found in the expected sub-bands and frequencies to quantify the third

order distortion present in the amplifier output signal 118. This measure of third order distortion is then output from the signal analyser 220 and can be used to control an external amplifier linearisation means. Similarly, the signal analyser 220 can determine where it expects to find fifth order, etc., distortion due to the amplifier input signal 112.

It will be appreciated that scheme 200 can be used to build up a spectrum of the residual distortion across the entire frequency band of input signal 112 by calculating the residual distortion spectra at different sub-bands by varying the frequency of local oscillator signal produced by synthesiser 128. As with detection scheme 100, in detection scheme 200, the series of sub-bands dictated by local oscillator signal 128 may be overlapping, abutting or providing coverage of only parts of the frequency band of input signal 112. It may be desirable to move through the series of sub-bands in a sequence which is pseudo random. In this way, each sub-band is processed at a pseudo randomised time. This avoids any interactive effects between the sequence of sub-bands sampled and the temporal characteristics of the input signal 112. In general, it is also possible for the sub-bands to be of unequal or adjustable frequency width to allow different parts of the frequency band of the input signal 112 to be covered with differing resolutions. This may assist the optimisation of distortion suppression and convergence speed (of a distortion reduction mechanism) for certain regions of the frequency band of the input signal 112 where linearisation is considered more important.

The detection schemes described herein may also be rendered adaptive by, for example, arranging that the sub-band bandwidths and/or sub-band sequences adapt to the occupancy of the frequency band of the input signal 112. An adaptive bandwidth scheme is facilitated by digital implementations of the filtering operations within the DCFD functions. This allows the filtering operations to have an adjustable bandwidth. The resolutions and integration intervals of the FFT operations can also be adjusted to adapt the frequency resolution within a sub-band or to vary the data collection time at each sub-band frequency.

An adaptive distortion detection scheme would advantageously allow a distortion suppression mechanism to achieve better distortion suppression and convergence speed.

Adaption would allow distortion detection sensitivity and performance to be concentrated exactly where it is needed within the frequency band of the input signal 112. Thus, enhanced distortion suppression performance would be obtained over the portions of the frequency band which are of interest at the expense of other parts of the frequency band where distortion suppression is not required (perhaps because the latter parts of the frequency band are not in use at a given time). Normally, it is observed that a linearised amplifier provides variable linearity across the band of interest. The above described adaptive techniques offer a way of steering the frequencies where better linearity can be obtained from the combination of the non-linear amplifier and the distortion suppression mechanism to match the frequencies occupied in input signal 112 at any one time. Therefore, the distortion suppression can be optimised to have the most effect at the frequencies of the strongest signals where it is most needed.

In a variation of detection schemes 100 and 200, the phase and amplitude, or inphase and quadrature (IQ) elements of the downconverted sub-band signals 134 and 136 can be detected and used to control both amplitude and phase (or both I and Q parts) of, for example, a phase and amplitude predistorter.

Claims

1.   A method of detecting distortion in an output signal which a signal handling means produces in response to an input signal, the method comprising isolating as a first signal a frequency range of the input signal and as a second signal a frequency range of the output signal, and comparing the first and second signals to assess the distortion in the output signal.

2.   A method according to claim 1, wherein the frequency ranges are substantially the same.

3.   A method according to claim 1 or 2, wherein the isolating step comprises frequency converting the input and output signals.

4.   A method according to any preceding claim, wherein the isolating step is performed at a series of different frequency ranges.

5.   A method according to claim 4, wherein at least two adjacent ranges in the series overlap.

6.   A method according to claim 4 or 5, wherein at least two adjacent ranges in the series abut.

7.   A method according to any of claims 4 to 6, wherein at least two adjacent ranges in the series are discontinuous.

8.   A method according to any one of claims 4 to 7, wherein the isolating step traverses the series in a sequence which is not in frequency order.

9.   A method according to any preceding claim, wherein at least one frequency range is chosen to cover a frequency range of interest.

10.   A method according to any preceding claim, wherein the at least one chosen range is chosen dynamically.

11.   A method according to claim 9 or 10, wherein the at least one chosen range is chosen to cover an active portion of frequency band of the input signal.

12.   A method according to claim 4, wherein the isolating step comprises sweeping the frequency ranges to generate the series to cover a desired frequency band of the input and output signals.

13.   A method according to any preceding claim, wherein the comparing step comprises subtracting the first signal from the second signal to produce a residual signal.

14.   A method according to claim 13, wherein the subtracting step involves scaling the first and second signals relative to one another prior to the actual subtraction.

15.   A method according to claim 13 or 14, wherein the comparing step comprises assessing the energy present in the residual and first signals.

16.   A method according to any one of claims 1 to 12, wherein the comparing step comprises transforming the first and second signals to the frequency domain as first and second spectra, respectively.

17.   A method according to claim 16, wherein the comparing step comprises using the first spectra to predict frequencies of the output signal at which distortion is likely to occur and examining the second spectra for distortion at the predicted frequencies.

18.   A method according to any preceding claims, wherein the distortion is measured to provide a measurement of phase and/or amplitude distortion.

19.   A method according to any preceding claim, wherein the comparison step comprises measuring the distortion due to a distortion component in the output signal.

20. A method of reducing the appearance of distortion in an output signal which a signal handling means produces in response to an input signal, the method comprising using a distortion reduction mechanism on the signal handling means and controlling the distortion reduction mechanism using an assessment of the distortion in the output signal, wherein the assessment is provided by the method of any one of the preceding claims.

21. A method according to any preceding claim, wherein the signal handling means is an amplifying means.

22. Apparatus for detecting distortion in an output signal which a signal handling means produces in response to an input signal, the apparatus comprising means for isolating as a first signal a frequency range of the input signal and as a second signal a frequency range of the output signal, and comparing means for comparing the first and second signals to assess the distortion in the output signal.

23. Apparatus according to claim 22, wherein the frequency ranges are substantially the same.

24. Apparatus according to claim 22 or 23, wherein the isolating means comprises means for frequency converting the input and output signals.

25. Apparatus according to any one of claims 22 to 24, wherein the isolating means is arranged to isolate a series of different frequency ranges of the input and output signals.

26. Apparatus according to claim 25, wherein at least two ranges in the series overlap.

27. Apparatus according to claim 25 or 26, wherein at least two adjacent ranges in the series abut.

28. Apparatus according to any one of claims 25 to 27, wherein at least two adjacent ranges in the series are discontinuous.

29.   Apparatus according to any one of claims 25 to 28, wherein the isolating means is arranged to traverse the series in a sequence which is not in frequency order.

30.   Apparatus according to any one of claims 22 to 29, wherein at least one frequency range is chosen to cover a frequency range of interest.

31.   Apparatus according to any one of claims 22 to 30, wherein at the at least one chosen range is chosen dynamically.

32.   Apparatus according to claim 30 or 31, wherein the at least one chosen range may be chosen to cover an active portion of the frequency band of the input signal.

33.   Apparatus according to claim 25, wherein the isolating means is arranged to sweep the frequency ranges to generate the series to cover a desired frequency band of the input and output signals.

34.   Apparatus according to any one of claims 22 to 33, wherein the comparing means comprises means for subtracting the first signal from the second signal to produce a residual signal.

35.   Apparatus according to claim 34, wherein the subtracting means is arranged to scale the first and second signals relative to one another prior to the actual subtraction.

36.   Apparatus according to claim 34 or 35, wherein the comparing means comprises means for assessing the energy present in the residual and first signals.

37.   Apparatus according to any one of claims 22 to 33, wherein the comparing means comprises means for transforming the first and second signals to the frequency domain as first and second spectra, respectively.

38.   Apparatus according to claim 37, wherein the comparing means comprises processing means for using the first spectra to predict frequencies of the output signal at which distortion is likely to occur and examining the second spectra for distortion at the predicted frequencies.

39.   Apparatus according to any one of claims 22 to 38, wherein the comparing means is arranged to provide a measurement of phase and/or amplitude distortion.

40.   Apparatus according to any one of claims 22 to 39, wherein the comparing means comprises means for measuring the distortion due to a distortion component in the output signal.

41.   Apparatus for reducing the appearance of distortion in an output signal which a signal handling means produces in response to an input signal, the apparatus comprising a distortion reduction mechanism arranged to operate on the signal handling means, distortion detecting apparatus according to any preceding claim and control means for controlling the distortion reduction mechanism using an assessment of the distortion in the output signal, wherein the assessment is provided by the distortion detection apparatus.

42.   Apparatus according to any one of claims 22 to 41, wherein the signal handling means is an amplifying means.

43.   A method of detecting distortion substantially as hereinbefore described with reference to Figure 1 or Figure 2.

44.   A method of reducing the appearance of distortion substantially as hereinbefore described with reference to Figure 1 or Figure 2.

45.   Apparatus for detecting distortion as substantially hereinbefore described with reference to Figure 1 or Figure 2.

46.   Apparatus for reducing the appearance of distortion as substantially hereinbefore described with reference to Figure 1 or Figure 2.
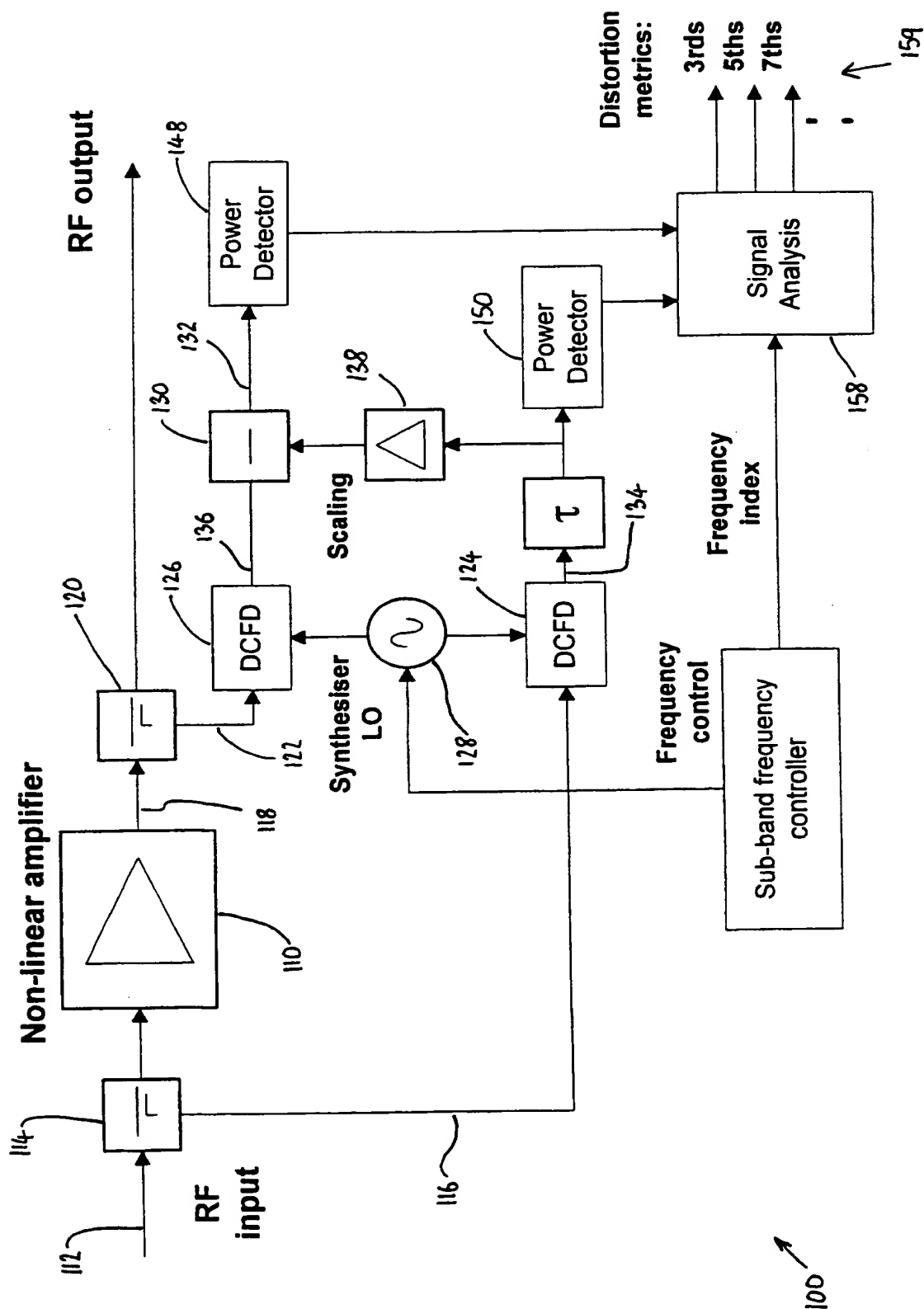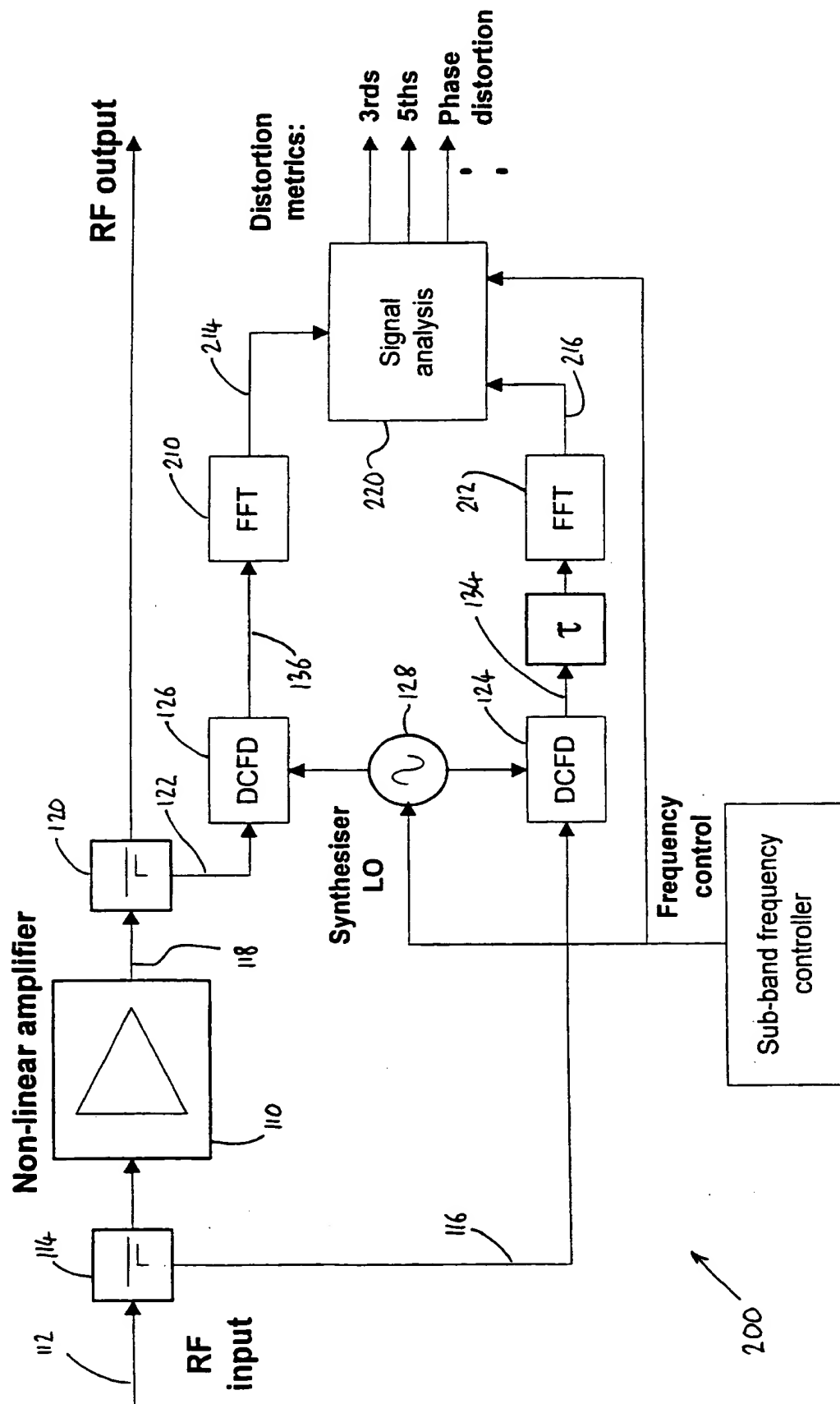
Figure 1

Figure 2

INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7   H03F1/32   H04B1/04

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7   H03F   H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 594 385 A (ANVARI KIOMARS) 14 January 1997 (1997-01-14)	1,2,5-9, 11-13, 16, 18-23, 30,32, 34,36, 37,39-46
A	abstract	3,4,10, 14,15, 17, 24-29, 31,33, 35,38
	column 2, line 66 –column 8, line 33 figures 5,6 ---	
	-/--	

[X] Further documents are listed in the continuation of box C.   [X] Patent family members are listed in annex.

° Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

6 July 2001

Date of mailing of the international search report

16/07/2001

Name and mailing address of the ISA
European Patent Office. P.B. 5818 Patentlaan 2
NL – 2280 HV Rijswijk
Tel. (+31–70) 340–2040. Tx. 31 651 epo nl.
Fax: (+31–70) 340–3016

Authorized officer

Tzimeas, K

INTERNATIONAL SEARCH REPORT

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication,where appropriate, of the relevant passages	Relevant to claim No.
P,X	EP 1 067 676 A (CIT ALCATEL) 10 January 2001 (2001-01-10)	1,2,5-9, 13,15, 18-23, 30,34, 36,39-46
P,A	abstract	3,4, 10-12, 14,16, 17, 24-29, 31-33, 35,37,38
	column 4, line 36 -column 8, line 24 claim 8 figure 3	
A	US 4 412 185 A (GERARD ROGER E J) 25 October 1983 (1983-10-25) abstract column 1, line 66 -column 5, line 61 figure 1	1-46

INTERNATIONAL SEARCH REPORT

Information on patent family members

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
US 5594385	A	14-01-1997	US	5485120 A	16-01-1996
			AU	3146695 A	22-02-1996
			AU	3147195 A	22-02-1996
			WO	9603802 A	08-02-1996
			WO	9603804 A	08-02-1996
			US	5610554 A	11-03-1997
			AU	3146895 A	22-02-1996
			WO	9603803 A	08-02-1996
EP 1067676	A	10-01-2001	CN	1291000 A	11-04-2001
US 4412185	A	25-10-1983	GB	2080062 A	27-01-1982
			CA	1157535 A	22-11-1983

This Page Blank (uspto)